

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	0	Pietig-Achim.in. and @pd>20030128	USPAT; US-PGPUB; EPO; JPO	2003/09/07 17:11			0
2	BRS	L2	0	Pietig-A.in. and @pd>20030128	DERWENT	2003/09/07 17:12			0
3	BRS	L3	465	(710/301.ccls. 711/115.ccls. 711/104.ccls. 235/380.ccls. 902/39.ccls. 902/26.ccls. 902/25.ccls. 235/451.ccls. 235/441.ccls. 235/440.ccls. 235/492.ccls.) and @pd>20030128	USPAT; US-PGPUB	2003/09/07 17:13			0
4	BRS	L4	35	((ic chip smart) adj1 card) same contact\$1 same (contactless (contact adj less) (non adj contact) noncontact wireless rf radio electromagnetic\$4) same (memory storage data program\$3 function\$3 application\$1) same (access\$3 authoriz\$3 permit\$4 enabl\$3) and @pd>20030128	USPAT; US-PGPUB	2003/09/07 17:32			0
5	BRS	L5	17	((ic chip smart) adj1 card) same contact\$1 same (contactless (contact adj less) (non adj contact) noncontact wireless rf radio electromagnetic\$4) same (memory storage data program\$3 function\$3 application\$1) same (access\$3 authoriz\$3 permit\$4 enabl\$3) and @pd>20030128	EPO; JPO; DERWENT; IBM_TDB	2003/09/07 17:52			0
6	BRS	L6	2	((smart chip ic) adj1 card) same ((issuer host) near5 updat\$3) and @pd>20030128	USPAT; US-PGPUB	2003/09/07 17:58			0
7	BRS	L7	1	((smart chip ic) adj1 card) same ((issuer host) near5 updat\$3) and @pd>20030128	EPO; JPO; DERWENT; IBM_TDB	2003/09/07 18:36			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
8	BRS	L11	1	((smart chip ic circuit memory) adj card) near5 (single one) near5 (nonvolatile flash eeprom) near5 (program\$1 application\$1) near5 data	USPAT; US-PGPUB	2003/09/07 18:40			0
9	BRS	L12	49	((smart chip ic circuit memory) adj card) near5 (nonvolatile flash eeprom) near5 (program\$1 application\$1) near5 data	USPAT; US-PGPUB	2003/09/07 19:03			0
10	BRS	L13	13	((smart chip ic circuit memory) adj card) near5 (nonvolatile flash eeprom) near5 (program\$1 application\$1) near5 data	EPO; JPO; DERWENT; IBM_TDB	2003/09/07 19:31			0
11	BRS	L16	20	((ic smart circuit) adj1 card\$1) near5 ((plural multiple several many different) near3 (memor\$3 chip\$1) near5 ((one single) near5 (memor\$3 chip\$1)))	USPAT; US-PGPUB	2003/09/07 19:42			0
12	BRS	L17	6	((ic smart circuit) adj1 card\$1) near5 ((plural multiple several many different) near3 (memor\$3 chip\$1) near5 ((one single) near5 (memor\$3 chip\$1)))	EPO; JPO; DERWENT; IBM_TDB	2003/09/07 19:42			0